

REMARKS

Claims 8, 9, and 11-15 are presently pending. Claims 1-7, 10, and 16-20 are cancelled without prejudice.

Claims 12-15 were rejected under 35 U.S.C. § 112. Assignee has amended claim 12 and submits that this rejection is now overcome.

Claims 8, 9, and 11-15 were rejected under 35 U.S.C. § 102(b) as being anticipated by Sotheran. Assignee has amended claim 8 to recite, among other limitations, “wherein the display engine provides the first frame, if host processor does not provide the information regarding the second frame to the display engine before a first horizontal synchronization pulse following a vertical synchronization pulse associated with the second frame”.

Examiner indicated that Sotheran taught “wherein the display engine provides the first frame ... if host processor does not provide the information regarding the second frame to the display engine before a predetermined time” and “Regarding claim 10, Sotheran discloses the predetermined time comprises a first horizontal synchronization pulse following a vertical synchronization pulse associated with the second frame (see Column 295, Line 45 – Column 296, Line 40).” Office Action at 4.

Sotheran, Col. 296, Lines 17-21 recite that, “The display address generator requests a new display buffer, once every vsync, via a two-wire interface. If there is a buffer flagged as READY, then that will be allocated to display by the buffer manager. If there is no READY buffer, the previously displayed buffer will be repeated.” Emphasis Added.

In contrast, Assignee claims “wherein the display engine provides the first frame, if host processor does not provide the information regarding the second frame to the display engine before a first horizontal synchronization pulse following a vertical synchronization pulse associated with the second frame”. Emphasis Added. Assignee respectfully submits that “first horizontal synchronization pulse following a vertical synchronization pulse” is different from “every vsync”. Accordingly, Assignee respectfully traverses Examiner’s assertion that “Regarding claim 10, Sotheran discloses the predetermined time comprises a first horizontal synchronization pulse following a

vertical synchronization pulse associated with the second frame (see Column 295, Line 45 – Column 296, Line 40).”

Assignee respectfully requests that Examiner withdraw the rejection to claims 8, 9, and 11-15.

CONCLUSION

For at least the foregoing reasons, each of the pending claims are now in a condition for allowance and Examiner is requested to pass this case to issuance.

The Commissioner is hereby authorized to charge additional fees or credit overpayments to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

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Respectfully submitted,



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